

The KHN filter employing tunable transresistance amplifiers (TRAs)

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ABSTRACT

The KHN filter employing transresistance amplifiers (TRAs) is proposed and simulation results using HSPICE are demonstrated. This TRA is composed of three positive-type second generation current conveyors (CCII+'s) and a resistor for simulation. Simulation results that confirm the theoretical analysis are obtained.

Key words: *transresistance amplifier(TRA), positive-type 2nd generation current conveyor (CCII+).*

I. Introduction

Recently there is a growing interest to current-mode circuit applications [1], because the traditionally designing methods based on op-amps are no longer adequate. Also, it is well known that the bandwidth of a voltage mode op-amps depends on the closed-loop voltage gain. TRA is an essential building block [2-3] found wide applications in multipliers/dividers [4] and MOSFET-C integrators [5-6]. The time constant of a TRA is small and insensitive to parasitic capacitances. Furthermore, the operational transconductance amplifier (OTA), which is widely used as a basic VLSI circuit block [7-9], can also be substituted by a TRA, and its transfer characteristic is opposite to that of the OTA.

In this paper, we propose a biquad using TRAs, which is constructed with three CCII+'s and one resistor (R_m). The proposed KHN circuit can be simultaneously realized as lowpass, bandpass and highpass filters.

II. Circuit description

Figure 1(a), 1(b) show a CCII+ and a TRA, respectively. The characteristics of a CCII+ and a TRA can be represented by

$$\begin{bmatrix} I_Y \\ V_X \\ I_Z \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} V_Y \\ I_X \\ V_Z \end{bmatrix} \quad (\text{For a CCII+}) \quad (1)$$

$$\begin{bmatrix} V_+ \\ V_- \\ V_{out} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ R_m & -R_m & 0 \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ I_0 \end{bmatrix} \quad (\text{For a TRA}) \quad (2)$$

Obviously, both input terminals of a TRA are virtually grounded and the output voltage is the difference of two input currents multiplied by a tunable transresistance R_m . Thus, the TRA constructing with three CCII+'s and one resistor is shown in figure 2 for simulation.

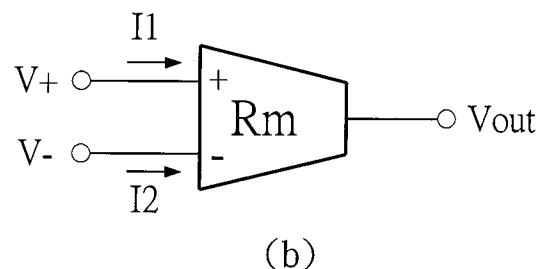
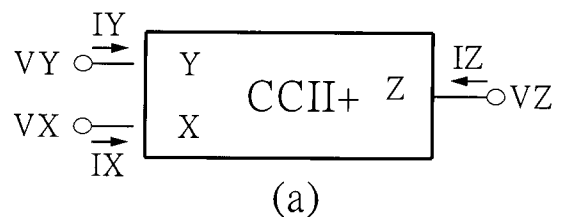


Fig. 1. symbol of (a) CCII+ and (b) TRA

We can then utilize the TRA (Figure 2) to implement the KHN biquad. The signal flow diagram of the biquad is shown in Figure 3. Figure 4 illustrates its actual circuit.

The transfer functions for this biquad can be obtained through Figure 3. The output voltage of Figure 4 can be expressed as

$$\frac{V_{lp}}{V_i} = \frac{\frac{R_3 R_{m3}}{R_2 R} + \frac{R_{m1} R_{m2} R_{m3}}{R_2 R^2}}{1 + \frac{R_{m1}}{R_f} + \frac{R_3 R_{m3}}{R_1 R^2} + \frac{R_{m1} R_{m2} R_{m3}}{R_2 R^2} + s(R_{m2} C + R_{m3} C + \frac{R_{m1} R_{m2} C}{R_f} + \frac{R_{m1} R_{m3} C}{R_f}) + S^2 C^2 R_{m2} R_{m3}}$$

$$\frac{V_{bp}}{V_i} = \frac{-S(R_3 R_{m3} C + \frac{R_{m1} R_{m2} R_{m3} C}{R})}{1 + \frac{R_{m1}}{R_f} + \frac{R_3 R_{m3}}{R_1 R^2} + \frac{R_{m1} R_{m2} R_{m3}}{R_2 R^2} + s(R_{m2} C + R_{m3} C + \frac{R_{m1} R_{m2} C}{R_f} + \frac{R_{m1} R_{m3} C}{R_f}) + S^2 C^2 R_{m2} R_{m3}}$$

$$\frac{V_{hp}}{V_i} = \frac{S^2 C^2 R_{m1} R_{m2} R_{m3}}{1 + \frac{R_{m1}}{R_f} + \frac{R_3 R_{m3}}{R_1 R^2} + \frac{R_{m1} R_{m2} R_{m3}}{R_2 R^2} + s(R_{m2} C + R_{m3} C + \frac{R_{m1} R_{m2} C}{R_f} + \frac{R_{m1} R_{m3} C}{R_f}) + S^2 C^2 R_{m2} R_{m3}}$$

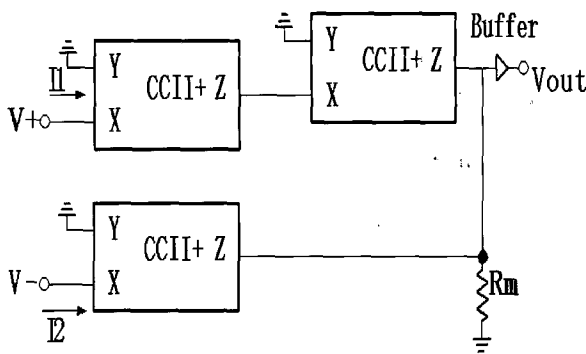


Fig. 2. TRA using CCII+'s

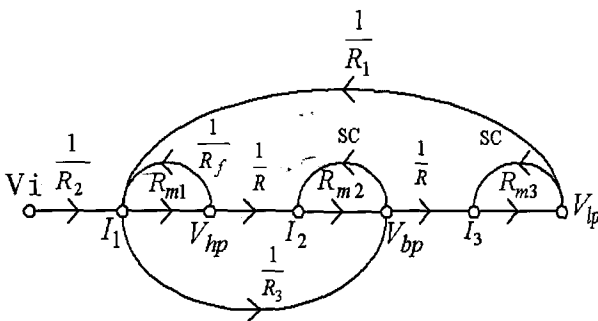


Fig. 3. The signal flow of the proposed KHN filter

III. Sensitivity analysis

By definition, The sensitivities of the frequency ω_0 and Q to the transresistance parameter R_{m1} of the first TRA are given by $S_{R_{m1}}^{\omega_0} = \frac{\partial \omega_0}{\partial R_{m1}} \frac{R_{m1}}{\omega_0} = \frac{1}{2}$ while

$$\frac{R_{m1} R_{m2} R_{m3}}{R_1 R_L} + \frac{R_{m1}}{R_f} \gg \frac{R_3 R_{m3}}{R_1 R} + 1 \quad S_{R_{m1}}^Q = \frac{\partial Q}{\partial R_{m1}} \frac{R_{m1}}{Q} = 1 \text{ while } R_{m1} + R_{m3} \gg R_{m1} \left(\frac{R_{m1} + R_{m3}}{R_f} \right) \text{ Likewise, } S_{R_{m2}}^{\omega_0}, S_{R_{m3}}^{\omega_0}, S_{R_{m2}}^Q, \text{ and } S_{R_{m3}}^Q \text{ can also be obtained in the same manner.}$$

IV. Simulation results

To confirm the theoretical analysis, we simulated the circuit presented in Figure 4 by means of H-spice. Meanwhile, we selected passive element values of,

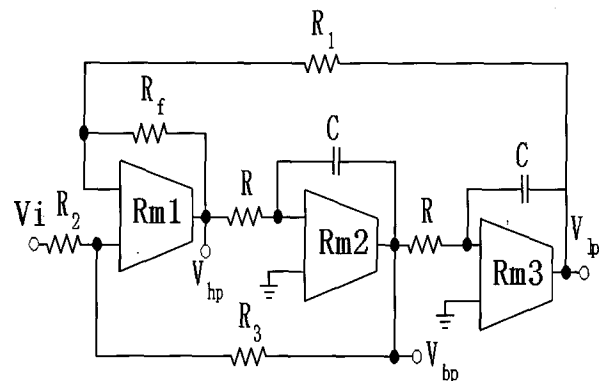


Fig. 4. TRA-based biquad filter

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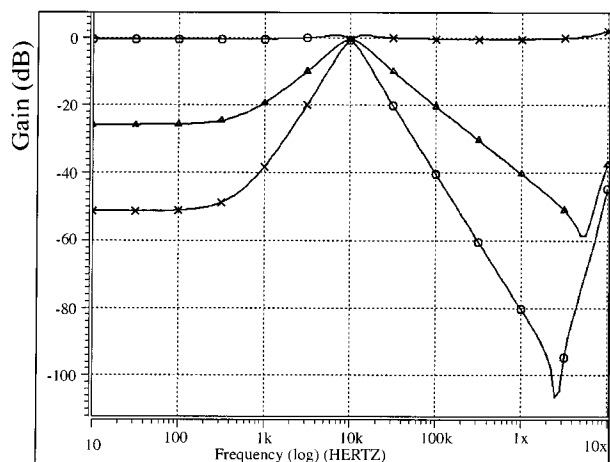


Fig. 5. HSPICE simulation results of the proposed circuit. Δ , simulated bandpass filter response; \circ , simulated lowpass filter response; \times , simulated highpass filter response.

$R_{m1} = R_{m2} = R_{m3} = 300k\Omega$, $R = R_1 = R_2 = T_f = 15.9k\Omega$, $R_3 = 16k\Omega$, $C = 1nF$. The power supply of a TRA is $\pm 5V$.

Figure 5 shows the simulation results. It appears that these results are consistent with the theoretical results. The difference in the high frequency region is stemming from the parasitic impedances of the current conveyors.

V. Conclusion

A new biquad using tunable TRAs for realizing lowpass, bandpass and highpass functions is presented. Simulation results which confirm the theoretical analysis are obtained.

References

1. M.T. Abuelma'atti, and M.H. Khan, 1997, New sinusoidal oscillators employing the CCII internal pole. *INT. J. ELECTRONICS*, **83**, 817-823.
2. J. Parnklang, C. Arammongkonwichai, and P. Kongtanasunthorn, 1998, Four-Quadrant CMOS ana-

log divider, *IEEE APCCS*, 271-274.

3. V. Riewruja, J. Parnklang, and A. Julprapa, 2001, Current tunable CMOS operational transresistance amplifier. *Proceedings. ISIE 2001. IEEE International Symposium*, **2**, 1328-1338.
4. R.A. Brannen, H. Elwan, and M. Ismail, 1999, A simple low-voltage all MOS linear-dB AGC/ Multiplier circuit. *IEEE International Symposium on Circuits and systems*, **2**, 318-321.
5. W. Chiu, J.H. Tsay, S.I. Liu, H.W. Tsao, and J.J. Chen, 1995, Single-capacitor MOSFET-C integrator using OTRA. *Electronics Letters*, **31**, 1796-1797.
6. J.J. Chen, H.W. Tsao, and S.I. Liu, 2001, Voltage-mode MOSFET-C filters using operational transresistance amplifiers (OTRAs) with reduced parasitic capacitance effect. *IEE Circuits, Devices and Systems*, *IEE Proceedings*, **148**, 242-249.
7. B. Gilbert, 1968, A new wide-band amplifier technique. *IEEE J. Solid-State Circuits*, **SC-3**, 353-365.
8. R. Caprio, 1973, Precision differential voltage current-converter. *IEE Electronics Letters*, **9**, 147-148.
9. A. Negungadi, and T.R. Viswanathan, 1984, Design of linear CMOS transconductance elements. *IEEE Transactions on Circuits system*, **CAS-31**, 891-894.

以可調轉阻放大器(TRAs)實現 KHN 濾波器

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摘 要

這篇提出以可調轉阻放大器(TRA)來設計的KHN濾波器，其模擬結果也以 HSPICE 驗證。因模擬時 TRA 是由三個第二代正型電流傳輸器(CCII+'s)及一個電阻器所構成，故其適用於高頻及IC實現。模擬結果也證實理論分析。

關鍵詞：轉阻放大器，第二代電流追隨器(正型)。